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JC984 U.S. PTO

LERNER AND GREENBERG, P.A.  
PATENT ATTORNEYS AND ATTORNEYS AT LAW

2445 Hollywood Boulevard  
Hollywood, Florida 33020  
Tel: (954) 925-1100  
Fax: (954) 925-1101

PATENTUSA®  
[www.patentusa.com](http://www.patentusa.com)  
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Laurence A. Greenberg (FL Bar)  
Werner H. Stemer (FL Bar)  
Ralph E. Locher (FL, IL, MO Bars)  
Gregory L. Mayback (FL Bar)

Manfred Beck (US & German Pat. Agent)  
Mark P. Weichselbaum (TN Bar)  
Markus Nolff (FL Bar)  
Loren Donald Pearson (FL Bar)  
Otto S. Kauder (Reg. Pat. Agent)  
Denise A. Lettau (DC Bar)  
Yonghong Chen (Chinese Pat. Agent)

Of Counsel: Herbert L. Lerner (NY Bar)

Mailing Address:  
Post Office Box 2480  
Hollywood, FL 33022-2480

New York Office  
153 E 57th Street  
Suite 15G  
New York, NY 10022

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I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to the U.S. Patent and Trademark Office, P.O. Box 2327, Arlington, VA 22202.

Docket No.: L&L-I0232

  
MICHAEL BURNS

Date: January 16, 2002

U.S. Patent and Trademark Office  
P.O. Box 2327  
Arlington, VA 22202

Sir:

Enclosed herewith are the necessary papers for filing the following application for Letters Patent:

Applicant : WOLFGANG RÖSNER ET AL.  
Title : METHOD FOR PRODUCING A VERTICAL SEMICONDUCTOR  
TRANSISTOR COMPONENT AND A VERTICAL SEMICONDUCTOR  
TRANSISTOR COMPONENT

5 sheets of formal drawings.

The payment in the amount of \$ 740.00 covering the filing fee.

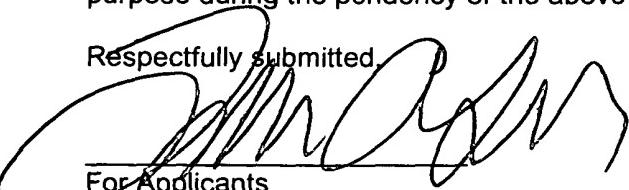
Information Disclosure Statement and 3 References.

PCT Cover Sheet WO 01/06542 A2 and WO 01/06542 A3.

This application is being filed without a signed oath or declaration under the provisions of 37 CFR 1.53(f). Applicants await notification of the date by which the oath or declaration and the surcharge are due, pursuant to this rule.

The Patent and Trademark Office is hereby given authority to charge Deposit Account No. 12-1099 of Lerner and Greenberg, P.A. for any fees due or deficiencies of payments made for any purpose during the pendency of the above-identified application.

Respectfully submitted,

  
For Applicants  
LAG:kf